

## Operational Amplifier Basics

As well as resistors and capacitors, **Operational Amplifiers**, or **Op-amps** as they are more commonly called, are one of the basic building blocks of Analogue Electronic Circuits. *Operational amplifiers* are linear devices that have all the properties required for nearly ideal DC amplification and are therefore used extensively in signal conditioning, filtering or to perform mathematical operations such as add, subtract, integration and differentiation.



An **Operational Amplifier**, or op-amp for short, is fundamentally a voltage amplifying device designed to be used with external feedback components such as resistors and capacitors between its output and input terminals. These feedback components determine the resulting function or “operation” of the amplifier and by virtue of the different feedback configurations whether resistive, capacitive or both, the amplifier can perform a variety of different operations, giving rise to its name of “Operational Amplifier”.

An *Operational Amplifier* is basically a three-terminal device which consists of two high impedance inputs, one called the **Inverting Input**, marked with a negative or “minus” sign, ( - ) and the other one called the **Non-inverting Input**, marked with a positive or “plus” sign ( + ).

The third terminal represents the operational amplifiers output port which can both sink and source either a voltage or a current. In a linear operational amplifier, the output signal is the amplification factor, known as the amplifiers gain ( A ) multiplied by the value of the input signal and depending on the nature of these input and output signals, there can be four different classifications of operational amplifier gain.

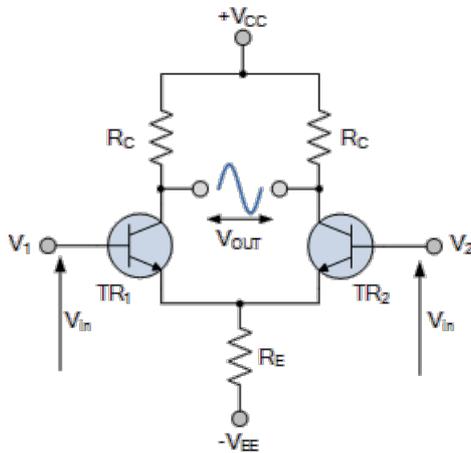
- Voltage – Voltage “in” and Voltage “out”
- Current – Current “in” and Current “out”
- Transconductance – Voltage “in” and Current “out”
- Transresistance – Current “in” and Voltage “out”

Since most of the circuits dealing with operational amplifiers are voltage amplifiers, we will limit the tutorials in this section

individual inputs. In other words, an op-amps output signal is the difference between the two input signals as the input stage of an Operational Amplifier is in fact a differential amplifier as shown below.

## Differential Amplifier

The circuit below shows a generalized form of a differential amplifier with two inputs marked  $V_1$  and  $V_2$ . The two identical transistors  $TR_1$  and  $TR_2$  are both biased at the same operating point with their emitters connected together and returned to the common rail,  $-V_{EE}$  by way of resistor  $R_E$ .



**Differential Amplifier**

The circuit operates from a dual supply  $+V_{CC}$  and  $-V_{EE}$  which ensures a constant supply. The voltage that appears at the output,  $V_{OUT}$  of the amplifier is the difference between the two input signals as the two base inputs are in *anti-phase* with each other.

So as the forward bias of transistor,  $TR_1$  is increased, the forward bias of transistor  $TR_2$  is reduced and vice versa. Then if the two transistors are perfectly matched, the current flowing through the common emitter resistor,  $R_E$  will remain constant.

Like the input signal, the output signal is also balanced and since the collector voltages either swing in opposite directions (anti-phase) or in the same direction (in-phase) the output voltage signal, taken from between the two collectors is, assuming a perfectly balanced circuit the zero difference between the two collector voltages.

This is known as the *Common Mode of Operation* with the **common mode gain** of the amplifier being the output gain when the input is zero.

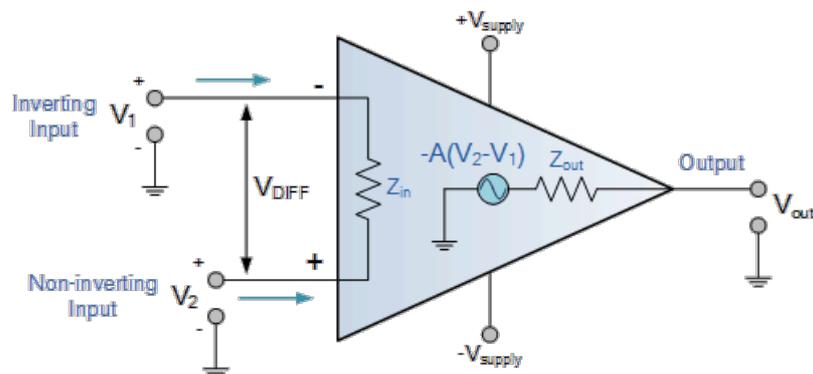
Operational Amplifiers also have one output (although there are ones with an additional differential output) of low impedance that is referenced to a common ground terminal and it should ignore any common mode signals that is, if an identical signal is applied to both the inverting and non-inverting inputs there should no change to the output.

However, in real amplifiers there is always some variation and the ratio of the change to the output voltage with regards to the change in the common mode input voltage is called the **Common Mode Rejection Ratio** or **CMRR**.

Operational Amplifiers on their own have a very high open loop DC gain and by applying some form of **Negative Feedback** we can produce an operational amplifier circuit that has a very precise gain characteristic that is dependant only on the feedback used. Note that the term "open loop" means that there are no feedback components used around the amplifier so the feedback path or loop is open.

An operational amplifier only responds to the difference between the voltages on its two input terminals, known commonly as the "*Differential Input Voltage*" and not to their common potential. Then if the same voltage potential is applied to both terminals the resultant output will be zero. An Operational Amplifiers gain is commonly known as the **Open Loop Differential Gain**, and is given the symbol ( $A_o$ ).

## Equivalent Circuit of an Ideal Operational Amplifier



## Op-amp Parameter and Idealised Characteristic

Open Loop Gain, ( $A_{vo}$ )

**Infinite** – The main function of an operational amplifier is to amplify the input signal and the more open loop gain it has the better. Open-loop gain is the gain of the op-amp without positive or negative feedback and for such an amplifier the gain will be infinite but typical real values range from about 20,000 to 200,000.

### Input impedance, ( $Z_{in}$ )

**Infinite** – Input impedance is the ratio of input voltage to input current and is assumed to be infinite to prevent any current flowing from the source supply into the amplifiers input circuitry ( $I_{in} = 0$ ). Real op-amps have input leakage currents from a few pico-amps to a few milli-amps.

### Output impedance, ( $Z_{out}$ )

**Zero** – The output impedance of the ideal operational amplifier is assumed to be zero acting as a perfect internal voltage source with no internal resistance so that it can supply as much current as necessary to the load. This internal resistance is effectively in series with the load thereby reducing the output voltage available to the load. Real op-amps have output impedances in the 100-20k $\Omega$  range.

### Bandwidth, (BW)

**Infinite** – An ideal operational amplifier has an infinite frequency response and can amplify any frequency signal from DC to the highest AC frequencies so it is therefore assumed to have an infinite bandwidth. With real op-amps, the bandwidth is limited by the Gain-Bandwidth product (GB), which is equal to the frequency where the amplifiers gain becomes unity.

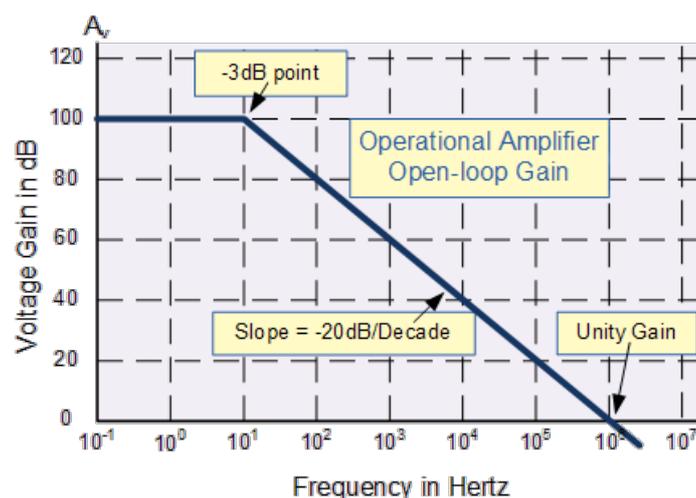
### Offset Voltage, ( $V_{io}$ )

**Zero** – The amplifiers output will be zero when the voltage difference between the inverting and the non-inverting inputs is zero, the same or when both inputs are grounded. Real op-amps have some amount of output offset voltage.

From these “idealized” characteristics above, we can see that the input resistance is infinite, so **no current flows into either input terminal** (the “current rule”) and that the **differential input offset voltage is zero** (the “voltage rule”). It is important to remember these two properties as they will help us understand the workings of the **Operational Amplifier** with regards to the analysis and design of op-amp circuits.

However, real **Operational Amplifiers** such as the commonly available **uA741**, for example do not have infinite gain or bandwidth but have a typical “Open Loop Gain” which is defined as the amplifiers output amplification without any external feedback signals connected to it and for a typical operational amplifier is about 100dB at DC (zero Hz). This output gain decreases linearly with frequency down to “Unity Gain” or 1, at about 1MHz and this is shown in the following open loop gain response curve.

### Open-loop Frequency Response Curve



From this frequency response curve we can see that the product of the gain against frequency is constant at any point along the curve. Also that the unity gain (0dB) frequency also determines the gain of the amplifier at any point along the curve. This constant is generally known as the **Gain Bandwidth Product** or **GBP**. Therefore:

$$\text{GBP} = \text{Gain} \times \text{Bandwidth} \text{ or } A \times \text{BW}.$$

For example, from the graph above the gain of the amplifier at 100kHz is given as 20dB or 10, then the gain bandwidth product is calculated as:

$$\text{GBP} = A \times \text{BW} = 10 \times 100,000\text{Hz} = 1,000,000.$$

Similarly, the operational amplifiers gain at 1kHz = 60dB or 1000, therefore the GBP is given as:

$$\text{GBP} = A \times \text{BW} = 1,000 \times 1,000\text{Hz} = 1,000,000. \text{ The same!}$$

The **Voltage Gain** ( $A_V$ ) of the operational amplifier can be found using the following formula:

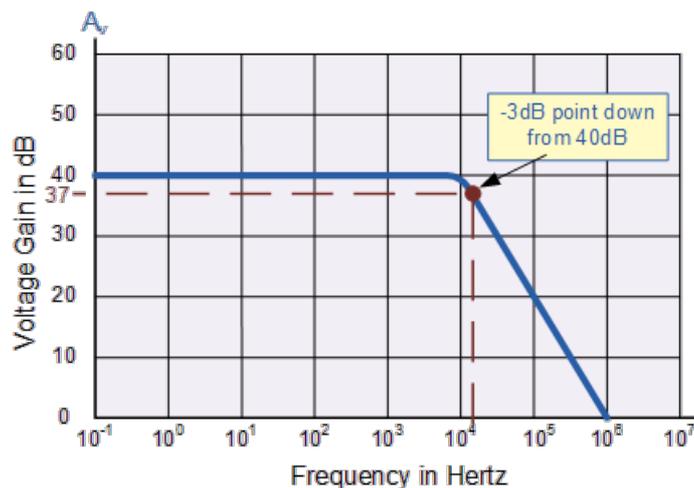
$$\text{Voltage Gain, (A)} = \frac{V_{\text{out}}}{V_{\text{in}}}$$

and in **Decibels** or (dB) is given as:

$$20 \log (A) \text{ or } 20 \log \frac{V_{\text{out}}}{V_{\text{in}}} \text{ in dB}$$

## An Operational Amplifiers Bandwidth

The operational amplifiers bandwidth is the frequency range over which the voltage gain of the amplifier is above 70.7% or -3dB (where 0dB is the maximum) of its maximum output value as shown below.



Here we have used the 40dB line as an example. The -3dB or 70.7% of  $V_{\text{max}}$  down point from the frequency response curve is given as 37dB. Taking a line across until it intersects with the main GBP curve gives us a frequency point just above the 10kHz line at about 12 to 15kHz. We can now calculate this more accurately as we already know the GBP of the amplifier, in this particular case 1MHz.

### Operational Amplifier Example No1.

Using the formula  $20 \log (A)$ , we can calculate the bandwidth of the amplifier as:

$$37 = 20 \log A \text{ therefore, } A = \text{anti-log} (37 \div 20) = 70.8$$

$$\text{GBP} \div A = \text{Bandwidth, therefore, } 1,000,000 \div 70.8 = 14,124\text{Hz, or } 14\text{kHz}$$

Then the bandwidth of the amplifier at a gain of 40dB is given as **14kHz** as previously predicted from the graph.

## Operational Amplifier Example No2.

If the gain of the operational amplifier was reduced by half to say **20dB** in the above frequency response curve, the -3dB point would now be at 17dB. This would then give the operational amplifier an overall gain of 7.08, therefore **A = 7.08**.

If we use the same formula as above, this new gain would give us a bandwidth of approximately **141.2kHz**, ten times more than the frequency given at the 40dB point. It can therefore be seen that by reducing the overall “open loop gain” of an operational amplifier its bandwidth is increased and visa versa.

In other words, an operational amplifiers bandwidth is inversely proportional to its gain, ( $A/ BW$ ). Also, this -3dB corner frequency point is generally known as the “half power point”, as the output power of the amplifier is at half its maximum value as shown:

$$\text{Power, } P = \left[ \frac{V^2}{R} \right] = \left[ I^2 \times R \right]$$

At  $f_c$   $V$  or  $I = 70.71\%$  of maximum

If  $R = 1$  and  $V$  or  $I = 0.7071\text{max}$

$$\text{Then: } P = \left[ \frac{(0.7071 \times V)^2}{1} \right] = \left[ (0.7071 \times I)^2 \times 1 \right]$$

$\therefore P = 0.5V$  or  $0.5I$  (half power)

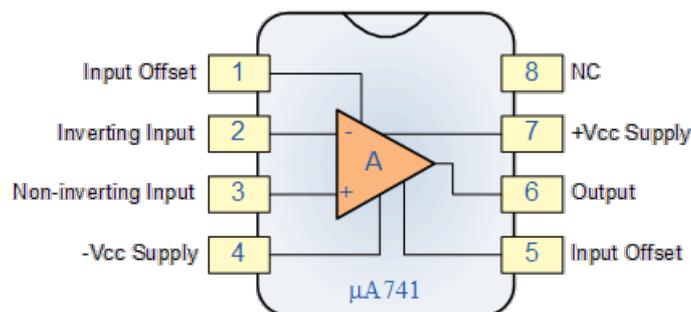
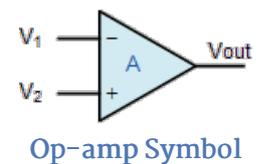
## Operational Amplifiers Summary

We know now that an **Operational amplifiers** is a very high gain DC differential amplifier that uses one or more external feedback networks to control its response and characteristics. We can connect external resistors or capacitors to the op-amp in a number of different ways to form basic “building Block” circuits such as, Inverting, Non-Inverting, Voltage Follower, Summing, Differential, Integrator and Differentiator type amplifiers.

An “ideal” or perfect operational amplifier is a device with certain special characteristics such as infinite open-loop gain  $A_o$ , infinite input resistance  $R_{in}$ , zero output resistance  $R_{out}$ , infinite bandwidth 0 to  $\infty$  and zero offset (the output is exactly zero when the input is zero).

There are a very large number of operational amplifier IC’s available to suit every possible application from standard bipolar, precision, high-speed, low-noise, high-voltage, etc, in either standard configuration or with internal Junction FET transistors.

Operational amplifiers are available in IC packages of either single, dual or quad op-amps within one single device. The most commonly available and used of all operational amplifiers in basic electronic kits and projects is the industry standard  **$\mu A$ -741**.



In the next tutorial about Operational Amplifiers, we will use negative feedback connected around the op-amp to produce a standard closed-loop amplifier circuit called an **Inverting Amplifier** circuit that produces an output signal which is 180° “out-of-phase” with the input.

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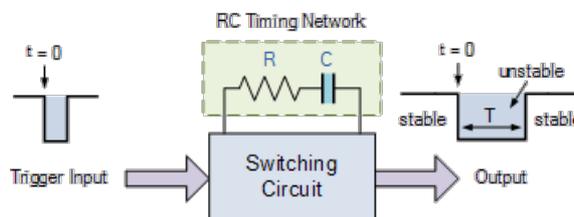
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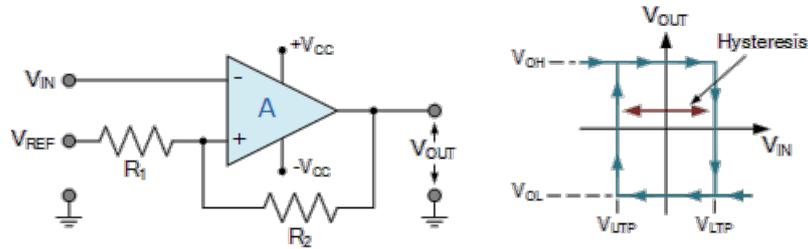
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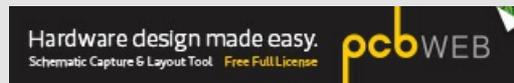
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