



| | Clamp: output | Back to Back:Output | Standard:Output |
|--------|--|--|---|
| Output | $V[\text{out_clamp}]$ | $V[\text{out_bak2bak}]$ | $V[\text{out_std}]$ |
| Input | $V[\text{in_clamp}]$ | $V[\text{in_bak2bak}]$ | $V[\text{in_std}]$ |
| Grid | $V[\text{g_clamp}]$ | $V[\text{g_bak2bak}]$ | $V[\text{g_std}]$ |
| | 92.4ms 93.0ms 93.6ms 94.2ms | 193ms 194ms | ms 297.0ms 297.9ms |
| | Input signal 200Vp-p Bias -39V Zeners: 86V (two 43V in series) | Input signal 200Vp-p Bias -39V Zeners: 90V (47V+ 43V in series) chosen to equal clamp case grid swing. | Input signal 200Vp-p Bias -39V Zeners: None |