



**Fig 5. Pin configuration SO16, SSOP16 and TSSOP16**

## 5.2 Pin description

**Table 2. Pin description**

Symbol	Pin	Description
GND	8	ground (0 V)
Q	9	serial data output
$\overline{\text{MR}}$	10	asynchronous master reset input (active LOW)
SHCP	11	shift register clock input (LOW-to-HIGH, edge-triggered)
STCP	12	storage register clock input (LOW-to-HIGH, edge-triggered)
$\overline{\text{PL}}$	13	parallel load input (active LOW)
DS	14	serial data input
D0, D1, D2, D3, D4, D5, D6, D7	15, 1, 2, 3, 4, 5, 6, 7	parallel data inputs
V <sub>CC</sub>	16	supply voltage