



CNSERV	
1	-15V
2	+15V
3	Fan-Voltage
4	GND
5	Lim_Off
6	GND
7	Lim_Serv_BH
8	Lim_Serv_AH
9	Lim_Serv_BL
10	Lim_Serv_AL
11	+U1
12	-U1
13	+U2
14	-U2

CN2LO	
1	Input- BL
2	Input+ BL
3	NTC BL
4	+15V
5	NTC AL
6	SOA-Sense
7	Input- AL
8	Input+ AL

CN2HI	
1	Input- BH
2	Input+ BH
3	NTC BH
4	+15V
5	NTC AH
6	SOA-Sense
7	Input- AH
8	Input+ AH

ALTERATIONS RESERVED!

				Last modified: 20.05.1999 10:57:02			86227C MAIN_PCB				
				Last plotted: 23.04.2004 12:03:48							
				1996	DATE	NAME	CIRCUIT DIAGRAM				1/4
				DSG'D	19.2.	M. Brandl					
				CHK'D	19.2.	M. Brandl					
				APP'D							
D	58/99	05.99	CSC	DYNACORD			SP3/5/7 / XA2600 / PM2600				2-
C	13/97	05.03.	Lang				351759				
B	60/96	08.05.	Brandl								
A	Nullserie	16.04.	Brandl								
SYMB. REVISION							DATE	NAME			