




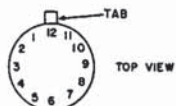
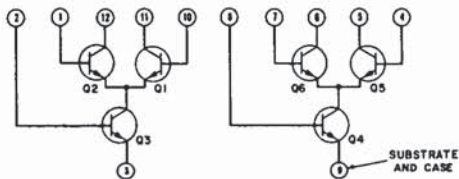


SCHEMATIC NOTES

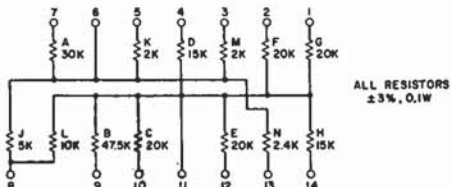
1. ALL RESISTORS ARE IN OHMS, 1/2W, ±5% UNLESS OTHERWISE NOTED.
2. ALL 1/4W AND 1/8W RESISTORS ARE ±1% UNLESS OTHERWISE NOTED.
3. ALL CAPACITORS ARE IN MICROFARADS UNLESS OTHERWISE NOTED.
4. REAR TERMINALS ARE SHOWN IN NORMAL STRAPPING FOR USE OF FRONT PANEL CONTROLS.
5. MODE SWITCH SHOWN IN INDEPENDENT POSITION; METER SWITCH SHOWN IN AMPS POSITION.
6. DC VOLTAGES WERE MEASURED UNDER THE FOLLOWING CONDITIONS:
 - A. HEWLETT-PACKARD 427A OR EQUIVALENT.
 - B. 115VAC INPUT.
 - C. SUPPLY IN CONSTANT VOLTAGE OPERATION AT MAXIMUM RATED OUTPUT VOLTAGE WITH NO LOAD CONNECTED.
 - D. CURRENT CONTROLS AND CROWBAR ADJUST CONTROLS TURNED FULLY CLOCKWISE; MODE SWITCH IN INDEPENDENT POSITION.
 - E. VOLTAGES REFERENCED TO +5 UNLESS OTHERWISE NOTED.
 - F. VOLTAGES ARE TYPICAL ±10% UNLESS OTHERWISE NOTED.
7. † DENOTES NOMINAL VALUE COMPONENT SELECTED FOR OPTIMUM PERFORMANCE.
8. * DENOTES CHASSIS MOUNTED COMPONENTS.
9. † DENOTES ±20PPM WIRE TEMPERATURE COEFFICIENT.
10.  ENCLOSURES FRONT PANEL MARKING.
11.  ENCLOSURES REAR PANEL MARKING.
12.  DENOTES FORWARD SIGNAL PATH OF THE VOLTAGE FEEDBACK LOOP.
13.  DENOTES FORWARD SIGNAL PATH OF THE CURRENT FEEDBACK LOOP.
14.  NUMBER OR LETTER REFERS TO PIN OF AJ1; NUMBER DESIGNATES CONTACT ON COMPONENT SIDE OF PLUG-IN BOARD; LETTER DESIGNATES CONTACT ON CIRCUIT SIDE OF PLUG-IN BOARD (SEE FIGURES 7-5 AND 7-6).
15. LETTERS (A) AND (B) ON A1 MOTHER BOARD AND A4 CROWBAR BOARD DESIGNATE COMPONENTS ASSOCIATED WITH MASTER AND SLAVE SUPPLIES, RESPECTIVELY.
16. PIN LOCATION DIAGRAM FOR INTEGRATED CIRCUIT Z1 IS SHOWN BELOW:



17. SCHEMATIC DIAGRAM FOR INTEGRATED CIRCUIT Z1 IS SHOWN BELOW:



18. SCHEMATIC DIAGRAM FOR INTEGRATED CIRCUIT Z2 IS SHOWN BELOW:



ALL RESISTORS ±3%, 0.1W

19. MODE SWITCH S2 AND METER SWITCH S3 AS VIEWED FROM REAR ARE SHOWN BELOW:



20. PIN LOCATIONS FOR TRANSISTORS, SCR, AND POWER DIODES ARE SHOWN BELOW:

