



NOTES

ALL POTS ARE LINEAR UNLESS NOTED
 ALL RESISTORS 1/4W UNLESS NOTED
 ALL RESISTORS IN OHMS UNLESS NOTED
 ALL CAPACITORS IN MFD UNLESS NOTED
 ALL DIODES IN4148 UNLESS NOTED
 LAST REF. DESIGNATOR USED:
 R90 C49 CR36 S7 Q8 U5 F1 J6
 VOLTAGES MAY VARY FROM UNIT TO UNIT
 VOLTAGES IN () ARE WITH FOOTSWITCH CLOSED
 CIRCUITRY PATENTS APPLIED FOR AND PENDING
 *PINS 1 AND 8 CONNECTED ON ALL TUBES
 **COMPONENTS DESIGNATED BY () INDICATE CHANGE
 ON BD. # 99001628

NOTES CONT'D.

20 mV MIN.
 V_I(IDLE) = 40 mV MAX.
 10 mV MAX. DIFFERENCE BETWEEN SIDES

75V MIN.
 V_K(IDLE) = 90V MAX.
 (6L6GT) 10V MIN. DIFFERENCE BETWEEN SIDES

TO MEASURE OUTPUT POWER RATING AT 5% T.H.D., T.P.1 MUST BE GROUNDED TO DEFEAT COMPRESSION EFFECT OF POWER AMP.
 BUILT UNDER U.S. PATENT NOS. 4,405,832 AND 4,439,742

LAST REF. DESIG. R7 C9 CR10 F4 Q1 S1
 BD. # 98801621

MX
 VTX SERIES
 BD. # 99001626
 **BD. # 99001628

PEAVEY ELECTRONICS CORP.	
MERIDIAN, MS.	
DRAWN BY	DATE 8/5/81
C.B. CHECK	
CHECK BY	
APPR. BY	

