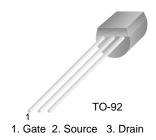


September 2007

J305 N-Channel RF Amplifier

- · This device is designed primarily for electronic switching applications such as low on resistance analog switching.
- Sourced from process 50.



Absolute Maximum Ratings* T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	30	V
V_{GS}	Gate-Source Voltage	-30	V
I_{GF}	Forward Gate Current	10	mA
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 ~ +150	°C

^{*} This ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case 125		°C/W
R _{0JA} Thermal Resistance, Junction to Ambient		357	°C/W

Electrical Characteristics* T_a=25°C unless otherwise noted Parameter

Off Characteristics					
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	-30		V
I _{GSS}	Gate Reverse Current	$V_{GS} = 20V, V_{DS} = 0$		-100	pА
V _{GS(off)}	Gate-Source Cut-off Voltage	$V_{DS} = 15V, I_{D} = 100nA$	-0.5	-3	V
V_{GS}	Gate-Source Forward Voltage	$V_{DS} = 15V, I_{D} = 0.2mA$	-1.5	-4.0	V

Test Condition

Min.

Max.

Units

On Characteristics

Symbol

*I _{DSS}	Zero-Gate Voltage Drain Current *	$V_{DS} = 15V, V_{GS} = 0$	1	8	mA

Small Signal Characteristics

gfs Forward Transferconductance $V_{DS} = 15V, V_{GS} = 0V$ 3000	
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¹⁾ These rating are based on a maximum junction temperature of 150 degrees C.

²⁾ These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

^{*} Pulse Test: Pulse Width ≤ 300μs, Duty Cycle = 2%





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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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