



4053 TRUE TABLE			
INPUTS		CHANNEL ON	
IN	SA/SB/SC		
L	L	ZA/ZB/ZC - Y0A/Y0B/Y0C	
L	H	ZA/ZB/ZC - Y1A/Y1B/Y1C	
H	X	NONE	

TEST POINT DESCRIPTION:			
TP1:	DSP RESET		
TP2:	DSP MUTE		
TP3:	SAFE MUTE CMD		
TP4:	I2C SCL		
TP5:	I2C SDA		

PROJECT NAME: DVA T4 INPUT			
PCB:	PST381D	Sheet:	1/1
File Name:	DVA_T4_INPUT_REV 0.5.DDB	Revision:	DEL 04/04/06 REV 0.5
Design:	Minelli M.	Checked:	Ottani C.
Production:			

Production:

Logo DB

Logo DB

Logo DB

Production:

Logo DB

Logo DB

Logo DB

CHANGING OBJECT FROM REV 04 TO REV 05:
NEW PCB REVISION 0.4 FROM PST381C TO PST381D
UPDATE COMPONENTS VALUE
ADDED EXT_PSU OFF CMD