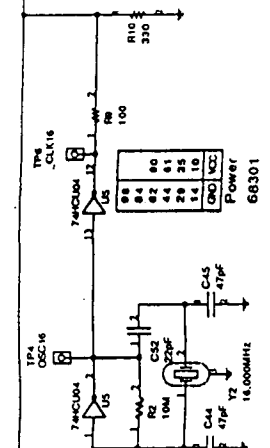
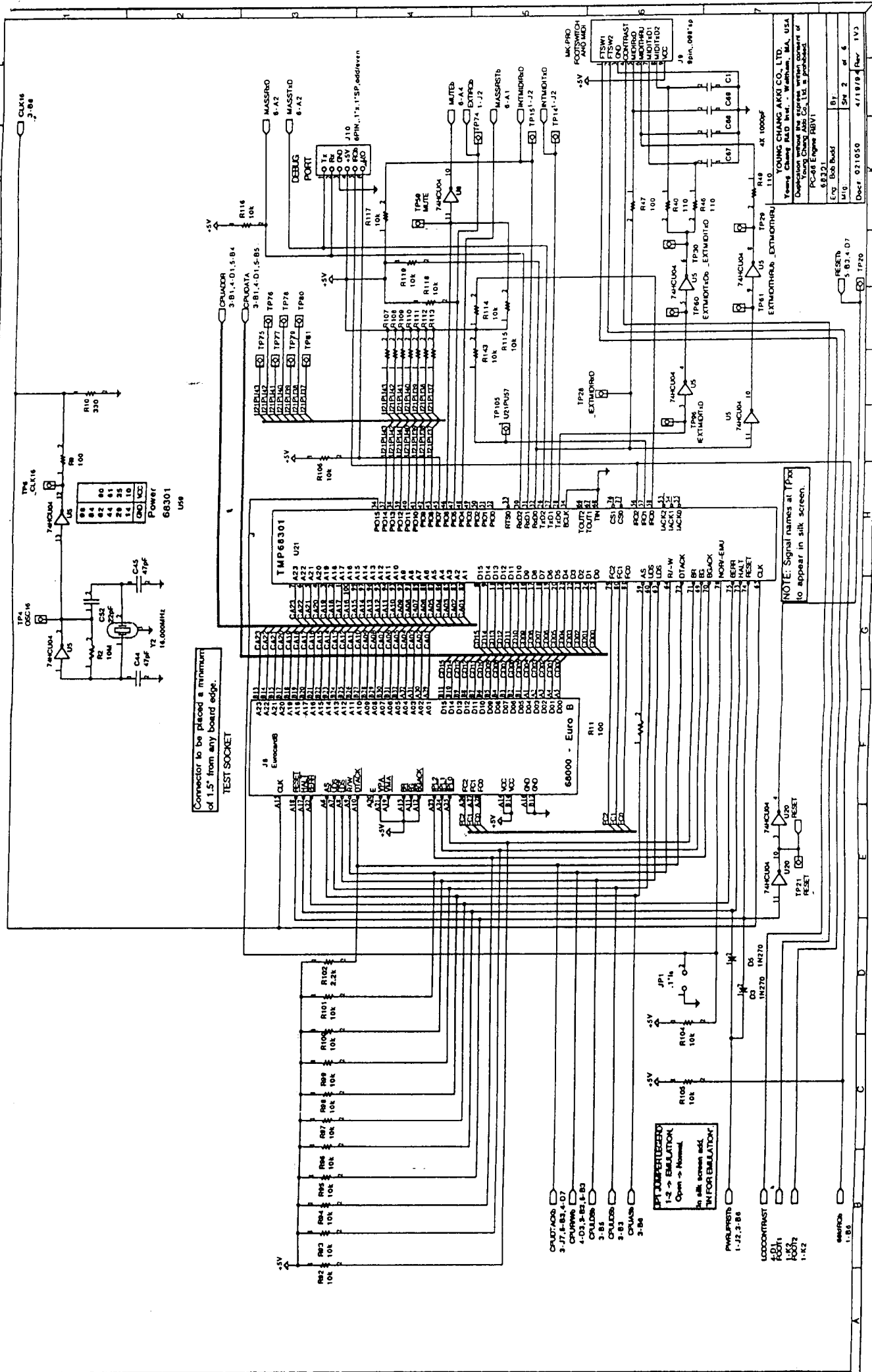
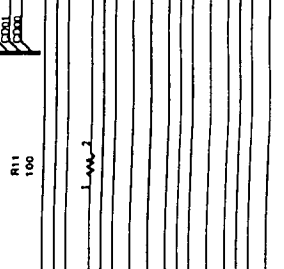
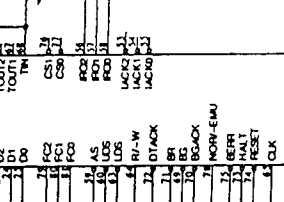
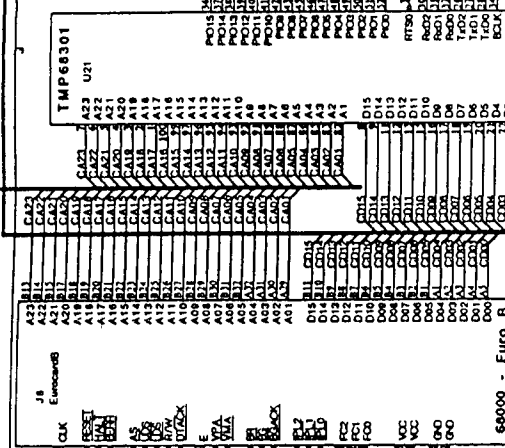


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 PC-88 Engine PCB1
 Scanner and Front Panel w/Processor
 Eng: Bob Budd By: _____
 Mfg: SM 1 of 6
 Decs: 021050 4/10/84 Rev: 1V3



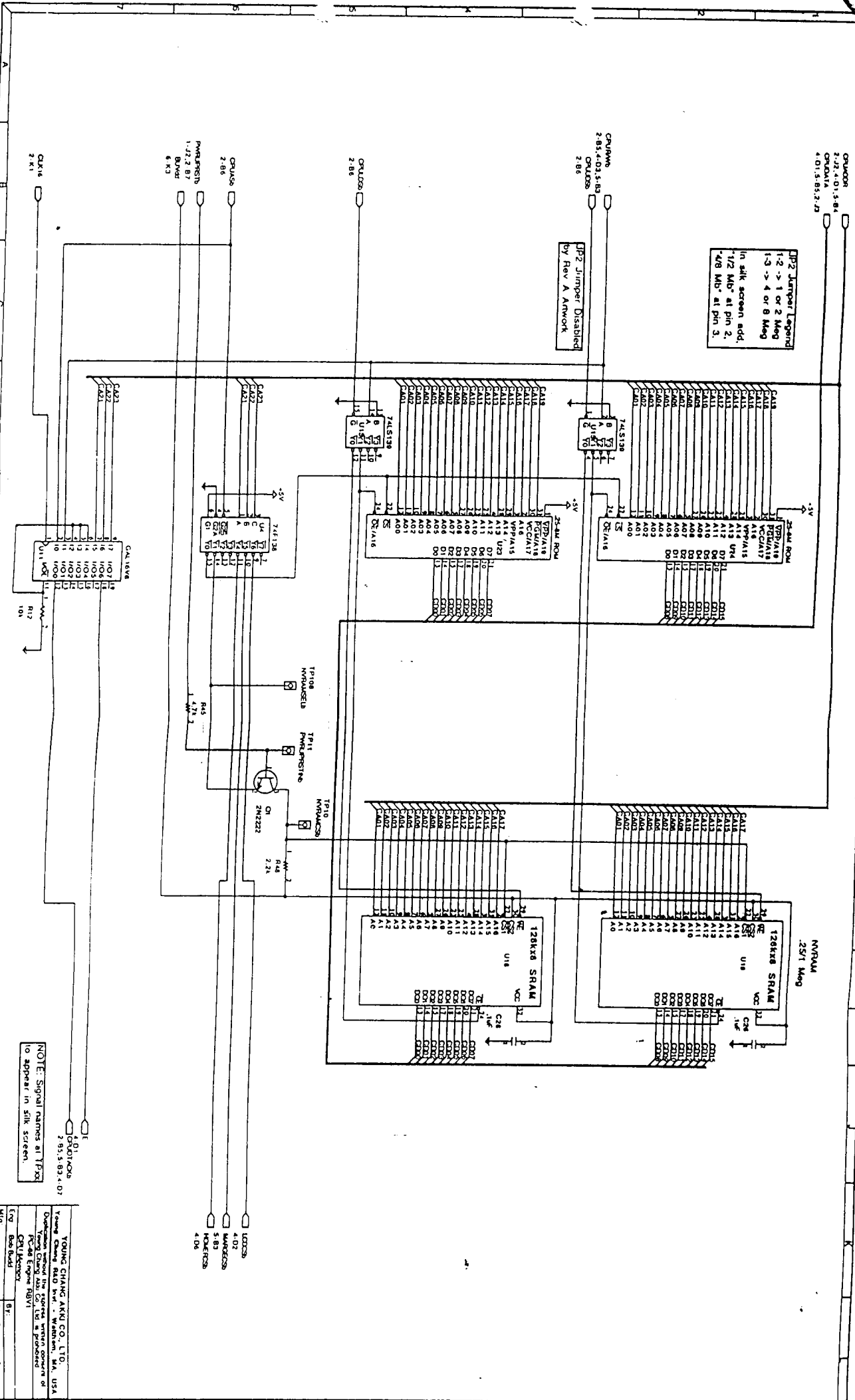
Connector to be placed a minimum of 1.5" from any board edge.

TEST SOCKET



NOTE: Signal names at J10 to appear in silk screen.

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 Department of
 PC-48 Engine RBV1
 58221
 Eng. Bob Budd
 M/G
 Decr. 02.10.50
 4/18/84 Rev 1W3
 Br
 Shr 2 of 6
 1W3



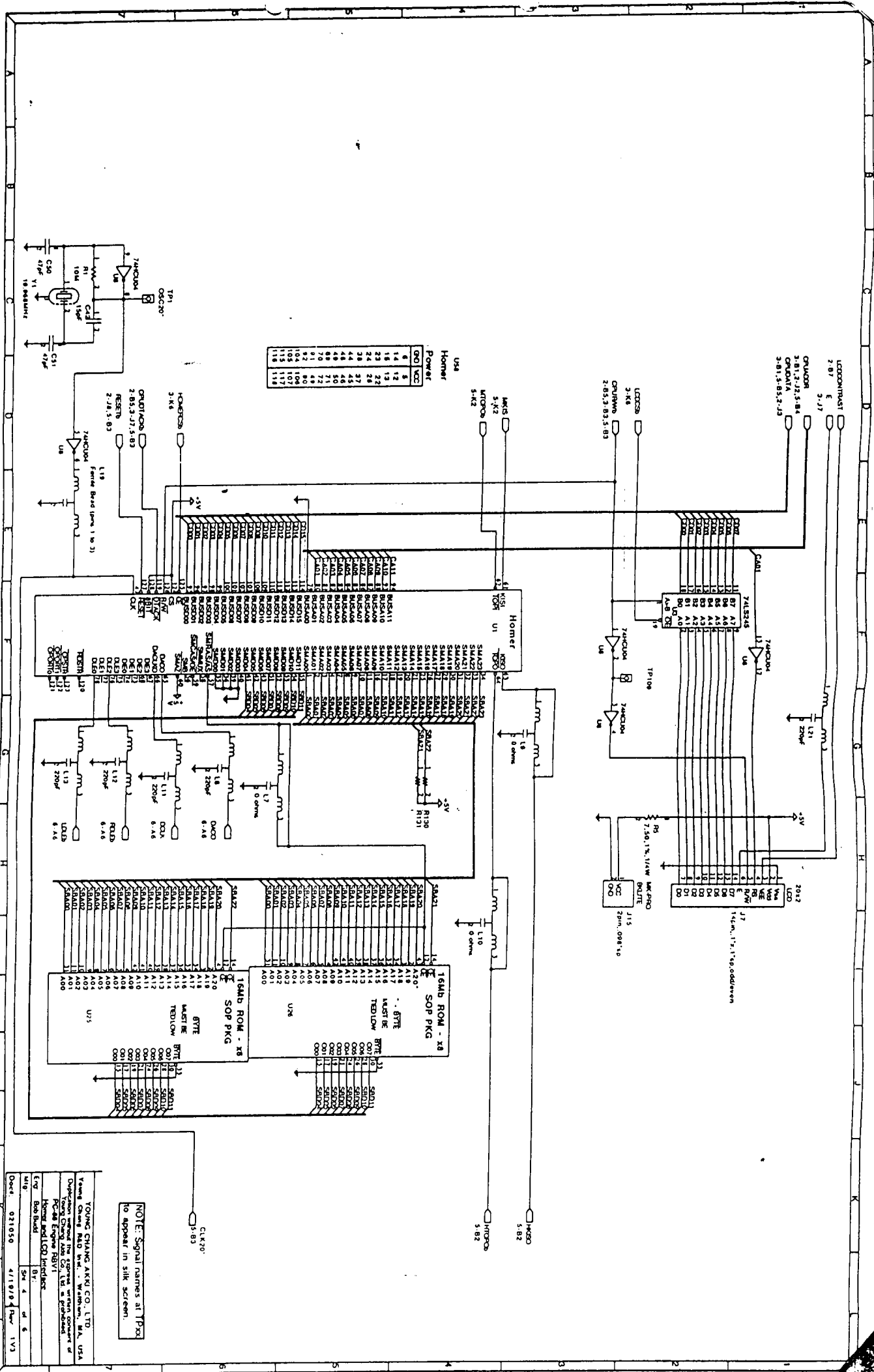
JP2 Jumper Legend
 1-2 -> 1 or 2 Meg
 1-3 -> 4 or 8 Meg
 In silk screen add
 1/2 Mb* at pin 2,
 4/8 Mb* at pin 3.

JP2 Jumper Disabled
 by Rev A Anwork

NOTE: Signal names at JP2X
 to appear in silk screen.

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Order 021050 4/19/84 Rev. 1V3

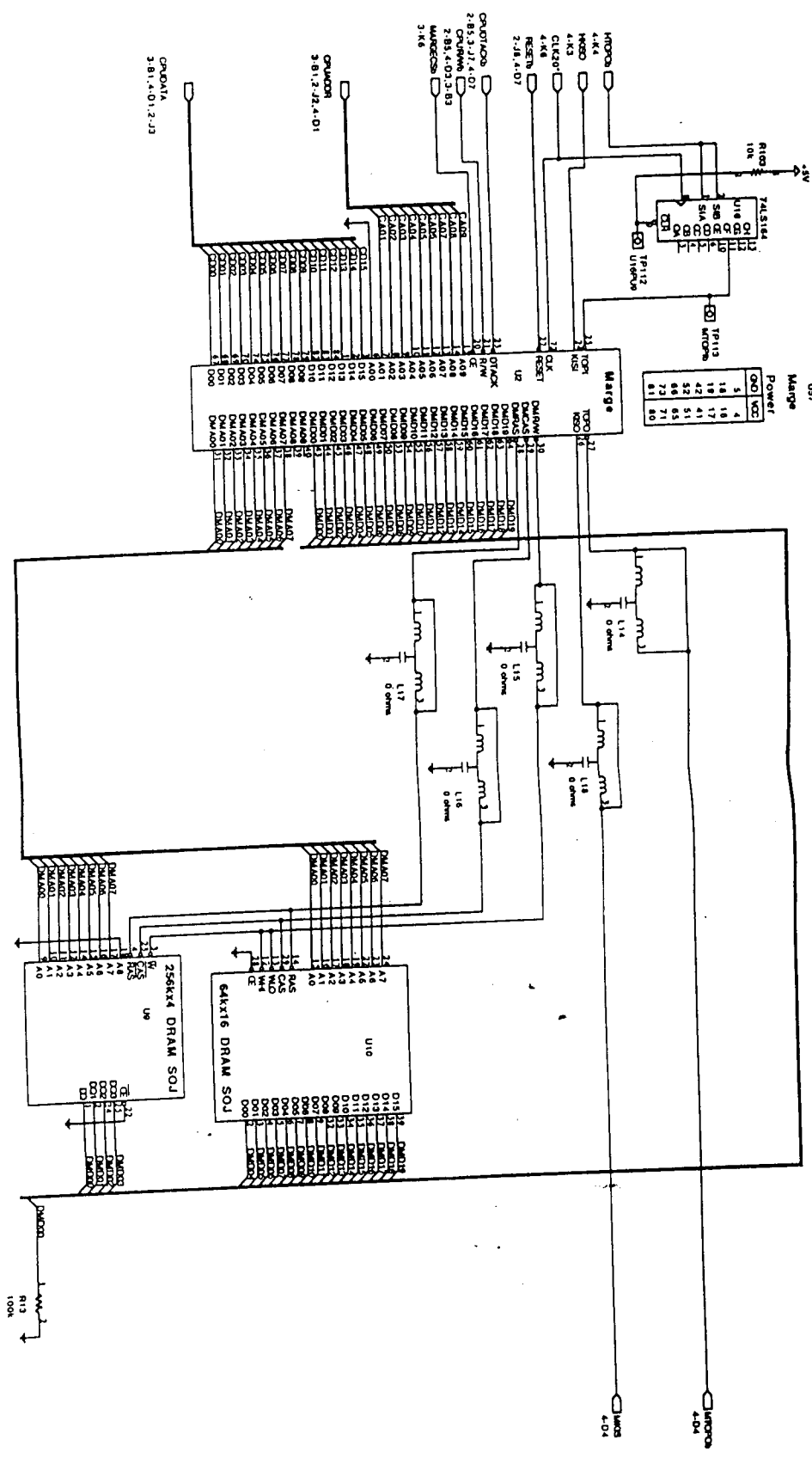


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118	118

USA
Homer
Power

NOTE: Signal names at 1Pcs to appear in silk screen.

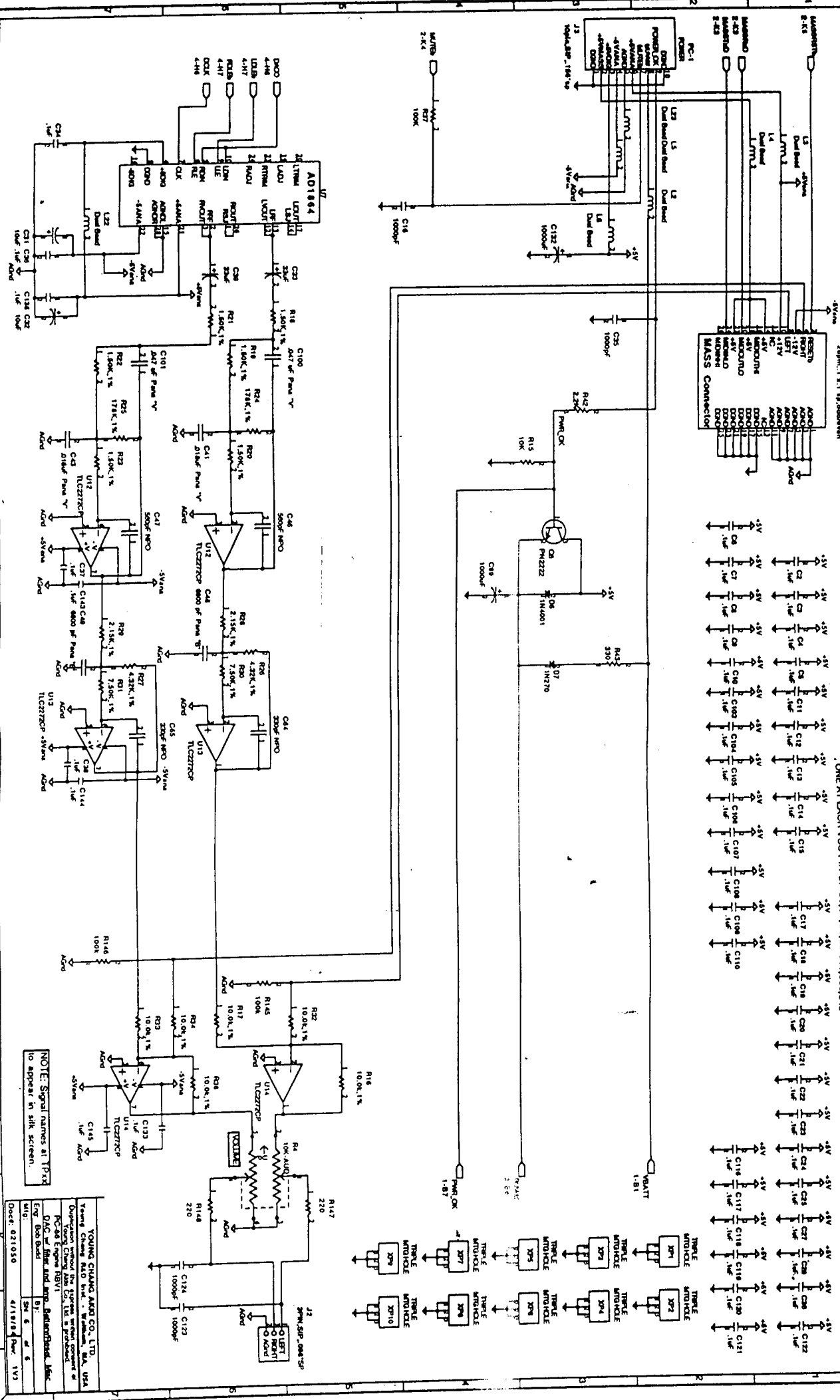
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PCB Design Engineer REAYT
Checked by: [Signature]
Est. No. [Number] Rev. 4 of 6
Date: 02/10/80 File: 143



NOTE: Signal names at PPA to appear in silk screen.

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 Hsinchu, Hsinchu County, Taiwan, R.O.C.
 YOUNG CHANG AKING CO., LTD. USA
 10000 Wilshire Blvd., Suite 1000, Los Angeles, CA 90024
 PC-251 Engine RSV1
 Hsinchu W. District, Taiwan
 BY: SM S M S
 Eng: Bao Budd
 Date: 021050 4/18/8 Rev: 1V3

DECOUPLING CAPS. ONE AT EACH GROUND PIN OF U1, U2, U4, U5, U6, U7, U8, U9, U11, U12, U13, U14, U15, U16, U17, U18, U19, U20, U21, U22, U23, U24, U25, U26, U27, U28, U29, U30, U31, U32, U33, U34, U35, U36, U37, U38, U39, U40, U41, U42, U43, U44, U45, U46, U47, U48, U49, U50.



NOTE: Signal names at TP's to appear in silk screen.

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 BY: S. S. at 6
 DATE: 02/10/80
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