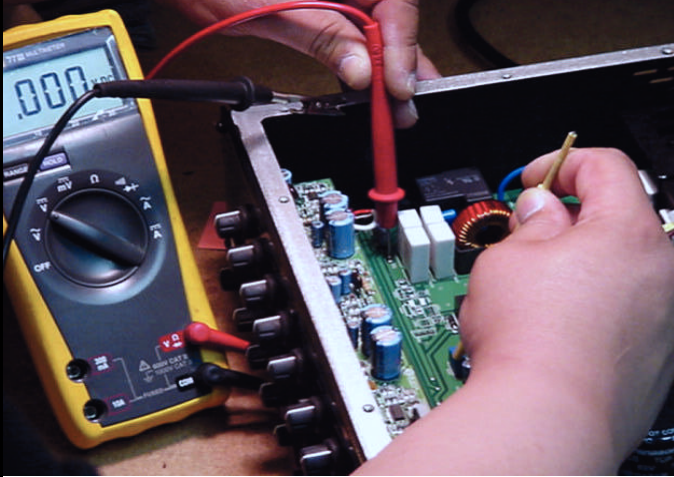
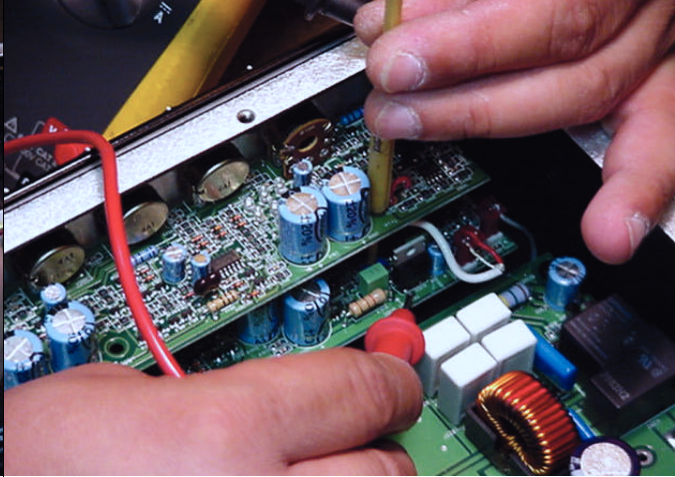
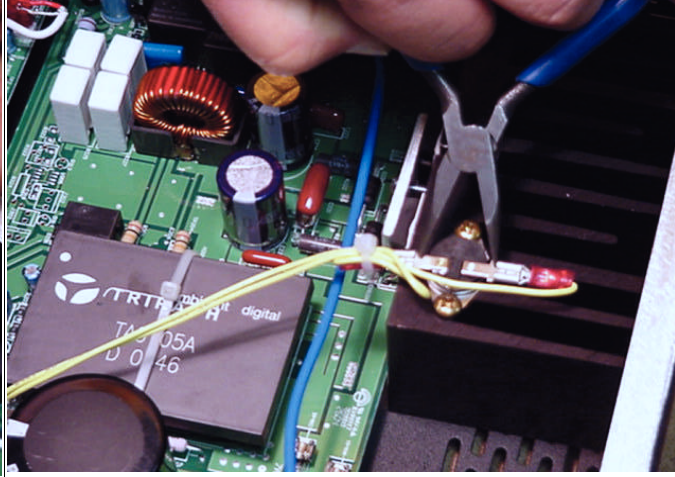
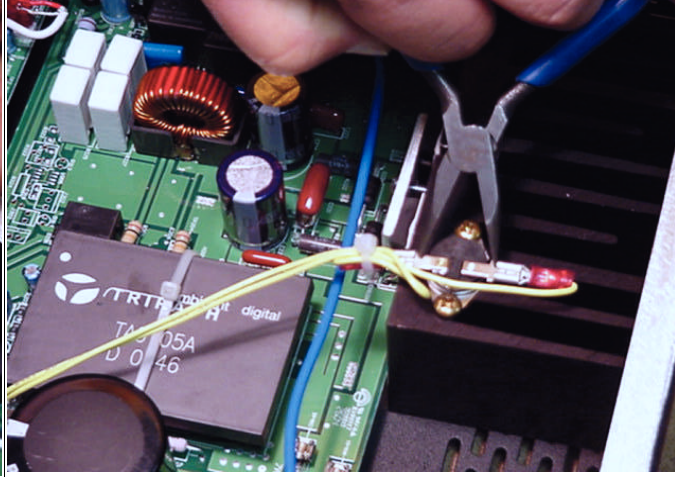
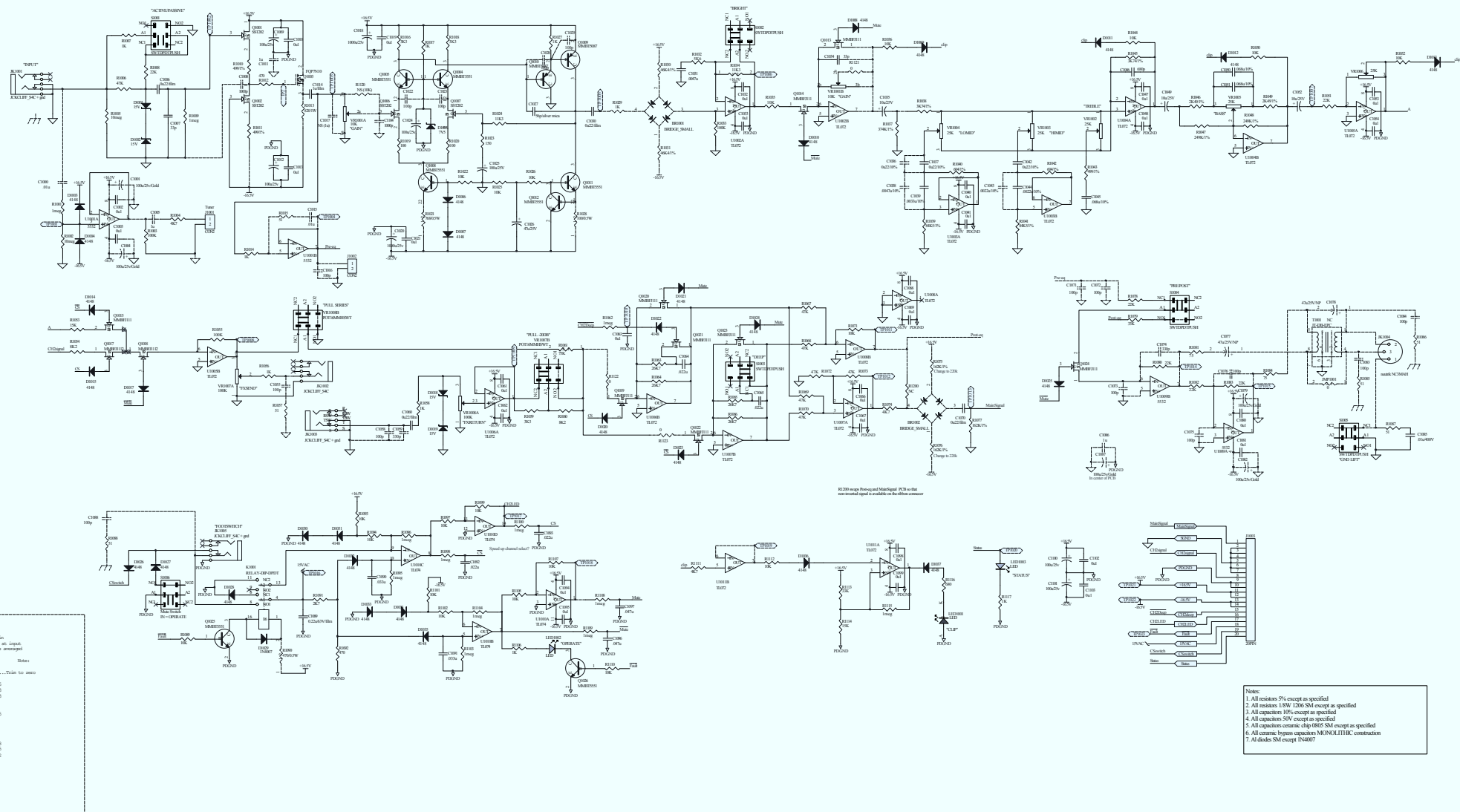


<b>AG500 TEST PROCEDURE</b>		18 MAR, 2005	
<b>Part 1: Quality Assurance</b>			
<b>Step</b>	<b>QA Check</b>	<b>Action</b>	
1	MOSFETs	Using feeler gauge, check that MOSFETs are flush against ceramic substate, and that substrate is flush against heat sink	
2	Thermal switch	Verify that thermal switch terminals are tight and that yellow wires do not touch heat sink	
3	Knobs	Verify all knob position	
4	Switches	Verify all switches function mechanically	
5	LED lenses	Verify all LED lenses are flush with faceplate	
6	AC jumpers	Verify AC jumpers on AG5JK are configured correctly for the target AC voltage	
<b>Part 2: Adjustments and Functional Test</b>			
All AG500 AC cords go to power strip. Power strip goes to variac. No other cables connected			
Step 1: Voltage test	AG500 AC cord in power strip and power strip to variac	Slowly variac unit up to the target voltage .NOTE VARIAC NEEDLE SHOULD NOT MOVE	
Step 2: Trim offset at TP1	Step 3: Trim offset at TP2	Step 4: Short thermal switch	
Action: Adjust multiturn trimmer R203	Action: Adjust trimmer VR1009 using special tweaker	Action: Short thermal switch with needle nose pliers	
Specification: 0VDC +/- .005V	Specification: 0VDC +/- .300V set as close to 0 as possible	Specification: Operate light goes off status light flashes	
			
			

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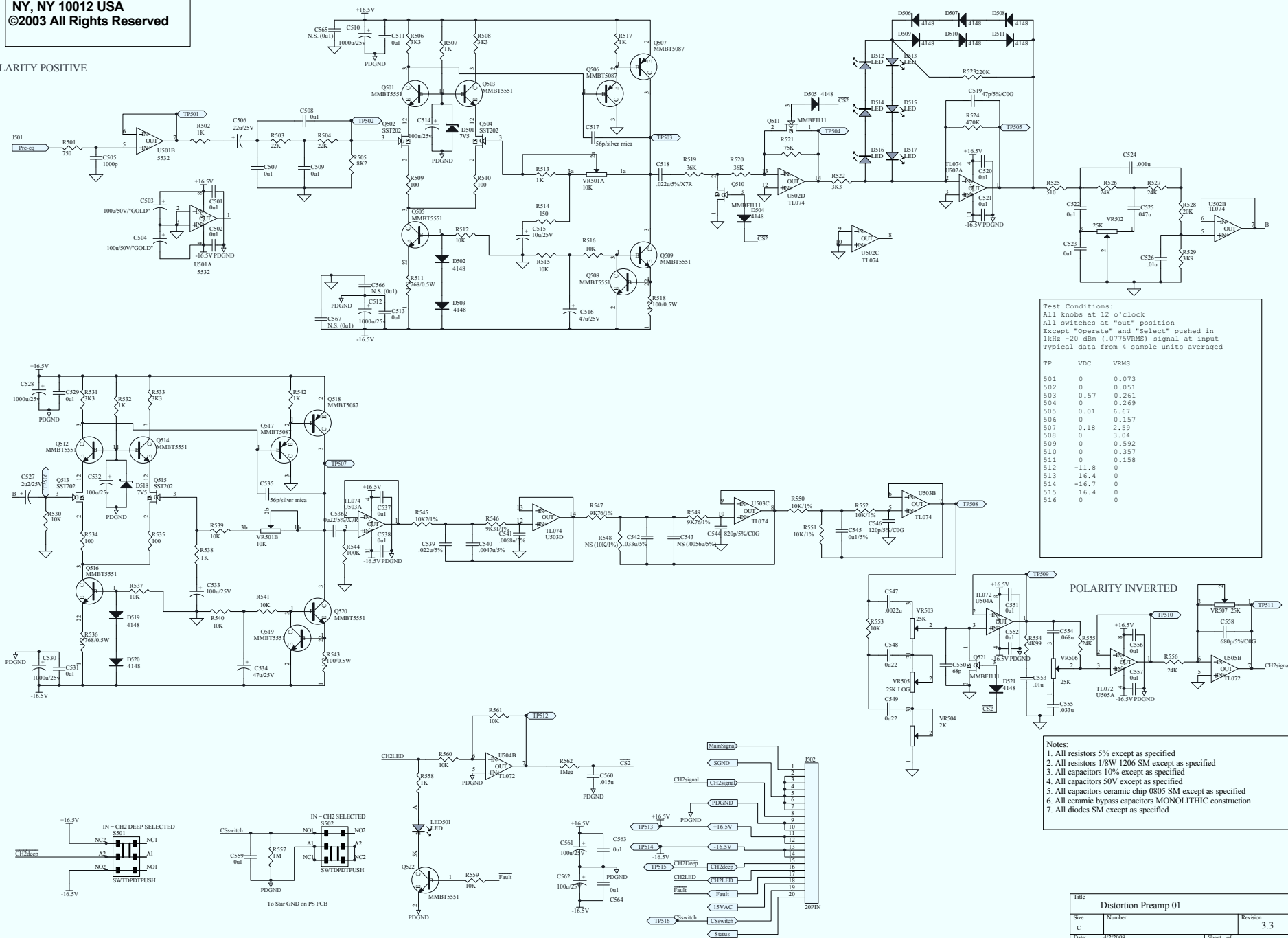
[illegible]

Notes:

1. All resistors 5% except as specified
2. All resistors 1/8W 1206 SM except as specified
3. All capacitors 10% except as specified
4. All capacitors 50V except as specified
5. All capacitors ceramic chip 0805 SM except as specified
6. All ceramic bypass capacitors MONOLITHIC construction
7. All diodes SM except 1N4007

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POLARITY POSITIVE



Test Conditions:  
All knobs at 12 o'clock  
All switches at "out" position  
Except "Operate" and "Select" pushed in  
1kHz -20 dBm (.0775VRMS) signal at input  
Typical data from 4 sample units averaged

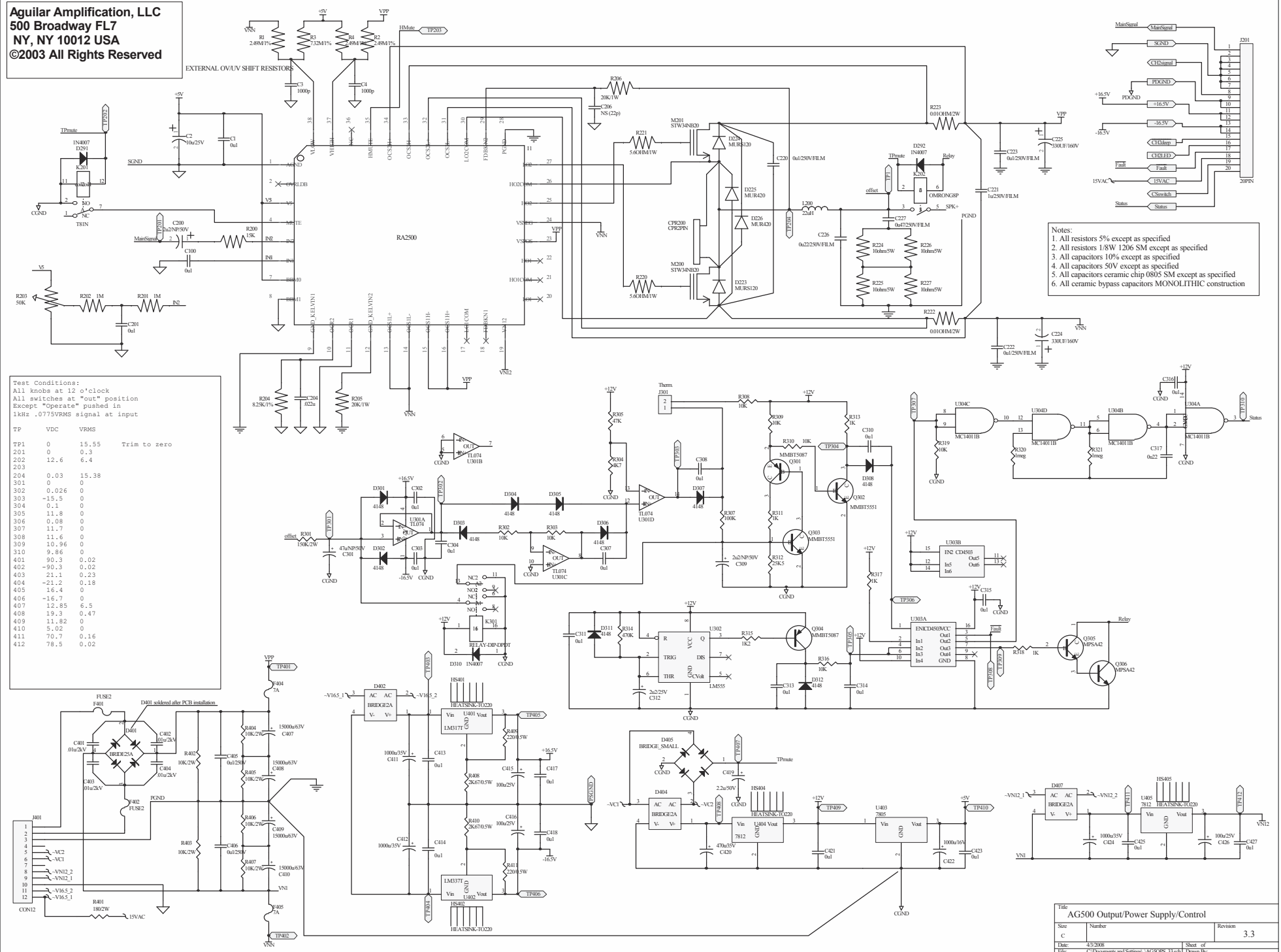
TP	VDC	VRMS
501	0	0.073
502	0	0.051
503	0.57	0.261
504	0	0.269
505	0.01	6.67
506	0	0.157
507	0.18	2.59
508	0	3.04
509	0	0.592
510	0	0.357
511	0	0.158
512	-11.8	0
513	16.4	0
514	-16.7	0
515	16.4	0
516	0	0

POLARITY INVERTED

Notes:

1. All resistors 5% except as specified
2. All resistors 1/8W 1206 SM except as specified
3. All capacitors 10% except as specified
4. All capacitors 50V except as specified
5. All capacitors ceramic chip 0805 SM except as specified
6. All ceramic bypass capacitors MONOLITHIC construction
7. All diodes SM except as specified

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Notes:

1. All resistors 5% except as specified
2. All resistors 1/8W 1206 SM except as specified
3. All capacitors 10% except as specified
4. All capacitors 50V except as specified
5. All capacitors ceramic chip 0805 SM except as specified
6. All ceramic bypass capacitors MONOLITHIC construction

```

Test Conditions:
All knobs at 12 o'clock
All switches at "out" position
Except "Operate" pushed in
1kHz .0775VRMS signal at input

```

TP	VDC	VRMS	
TP1	0	15.55	Trim to zero
201	0	0.3	
202	12.6	6.4	
203			
204	0.03	15.38	
301	0	0	
302	0.026	0	
303	-15.5	0	
304	0.1	0	
305	11.8	0	
306	0.08	0	
307	11.7	0	
308	11.6	0	
309	10.96	0	
310	9.86	0	
401	90.3	0.02	
402	-90.3	0.02	
403	21.1	0.23	
404	-21.2	0.18	
405	16.4	0	
406	-16.7	0	
407	12.85	6.5	
408	19.3	0.47	
409	11.82	0	
410	5.02	0	
411	70.7	0.16	
412	78.5	0.02	