



- CALIBRATION PROCEDURES:**
1. Connect a 4 ohm load resistor capable of dissipating 350 watts to one speaker output jack. Set impedance selector switch to 4 ohms.
 2. Output stage bias current adjustment. A) Adjust VR1 for +0.72 volts DC between T.P. 1 and ground. B) Adjust VR2 for zero ±0.01 volts between T.P. 1 and T.P. 2.
 3. Phase inverter balance control adjustment. A) Harmonic distortion meter method: Drive amplifier to 25 volts RMS out of 40 Hz and connect distortion meter to load resistor, adjust VR3 for minimum distortion. B) Voltmeter method: Adjust output to 8.9 rms, 3A and connect DC voltmeter between test points T.P. 1 and T.P. 2, adjust VR3 for zero ±0.01 volts.

- NOTES:**
- 1) THIS UNIT CONTAINS HAZARDOUS VOLTAGE. DISCONNECT POWER AND BE SURE POWER SUPPLY IS DISCHARGED BEFORE TOUCHING INTERNAL PARTS.
 - 2) UNLESS NOTED, RESISTOR VALUES IN OHMS, 1/4W-5% TOL. CAPACITOR VALUES IN MICROFARADS, 50V-10% TOL.
 - 3) VOLTAGES ARE MEASURED WITH 1 MEGOHM OSCILLOSCOPE AND 10 MEGOHM DIGITAL VOLTMETER. VOLTS IN RECTANGLES ARE RMS SIGNAL VOLTAGES WITH 0.2V 100C @ 20. OTHER VOLTAGES ARE DC WITH NO SIGNAL.

CAUTION:
THIS SCHEMATIC IS PROVIDED FOR USE BY QUALIFIED PERSONNEL TO AVOID RISK OF ELECTRIC SHOCK. REFER SERVICING TO QUALIFIED SERVICE PERSONNEL. DO NOT PERFORM ANY SERVICING BEYOND THAT EXPLAINED IN THE OPERATING INSTRUCTIONS.

PARTIAL PARTS LIST #97-419-00

DESIGNATOR	DESCRIPTION	SLM PART #
V1	12AX7	95-127-01
V2, V3	12AU7	95-126-01
V4-V9	6550	95-650-01
P1-P3	50K	70-503-20
P4	100 W	70-101-21
J1, J2	1/4" JACK	39-116-01
J3	XLR WALE	39-308-01
J4, J5	1/4" JACK	39-116-01
J6-J8	PN JACK	39-403-01
J9	2 PIN HDR	17-311-02
J10	6 PIN HDR	17-311-06
J11-J26	250 TAB	17-636-01
J27-J29	9 PIN SOCKET	17-430-09
J30-J35	8 PIN SOCKET	17-430-08
(3)	HEX STANDOFF	85-085-01
(12)	HEX STANDOFF	85-133-01

ORIG. DAN-7/24/89 *DM* *DM*
 VERS. DRAWN BY CHECKED BY APPROVED BY
 PAGE # 3 OF 3 DRAWING # 16-414-01
 DESCRIPTION: SVT-II PWR-AMP SCHEM.

SLM ELECTRONICS
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