

NOTES
 ALL POTS ARE LINEAR UNLESS NOTED
 ALL RESISTORS 1/4W UNLESS NOTED
 ALL RESISTORS IN OHMS UNLESS NOTED
 ALL CAPACITORS IN MFD UNLESS NOTED
 ALL DIODES IN 4148 UNLESS NOTED
 LAST REF. DESIGNATOR USED:
 R112 C66 CR38 S3 J6 Q9 J6

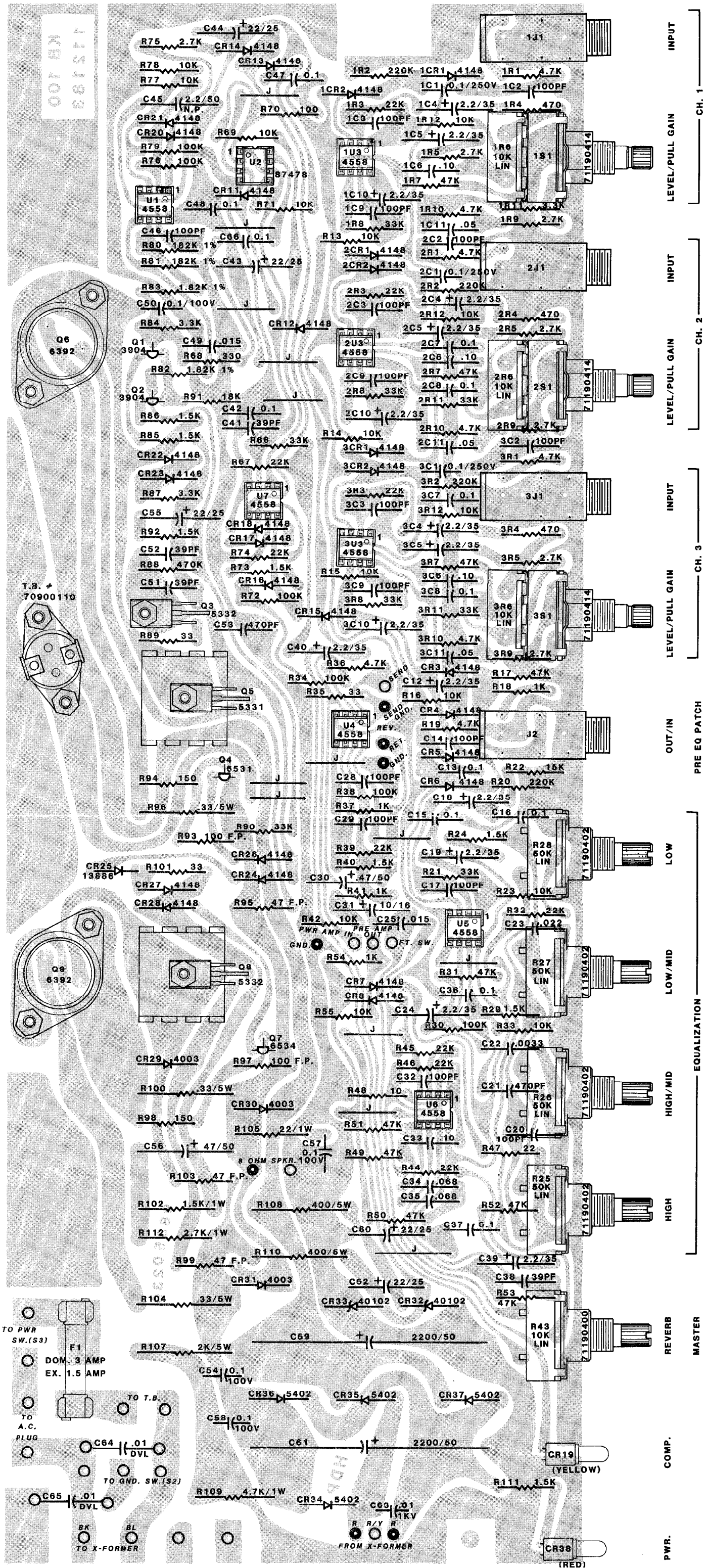
BUILT UNDER U.S. PATENT NO. 4,318,053

PEAVEY ELECTRONICS CORP.
 MERIDIAN, MS.
 DRAWN BY *RSB* DATE 2/28/84
 C.E. CK. *RSB*
 CK. BY *Ross*
 APPR. BY *RSB*

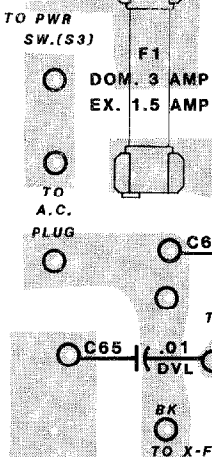
KB 100
 BD. # 98850230(11/21/83)

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T.B. # 70900110



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INPUT

LEVEL/PULL GAIN

INPUT

LEVEL/PULL GAIN

INPUT

LEVEL/PULL GAIN

OUT/IN

PRE EQ PATCH

LOW

LOW/MID

HIGH/MID

HIGH

REVERB MASTER

COMP.

PWR.