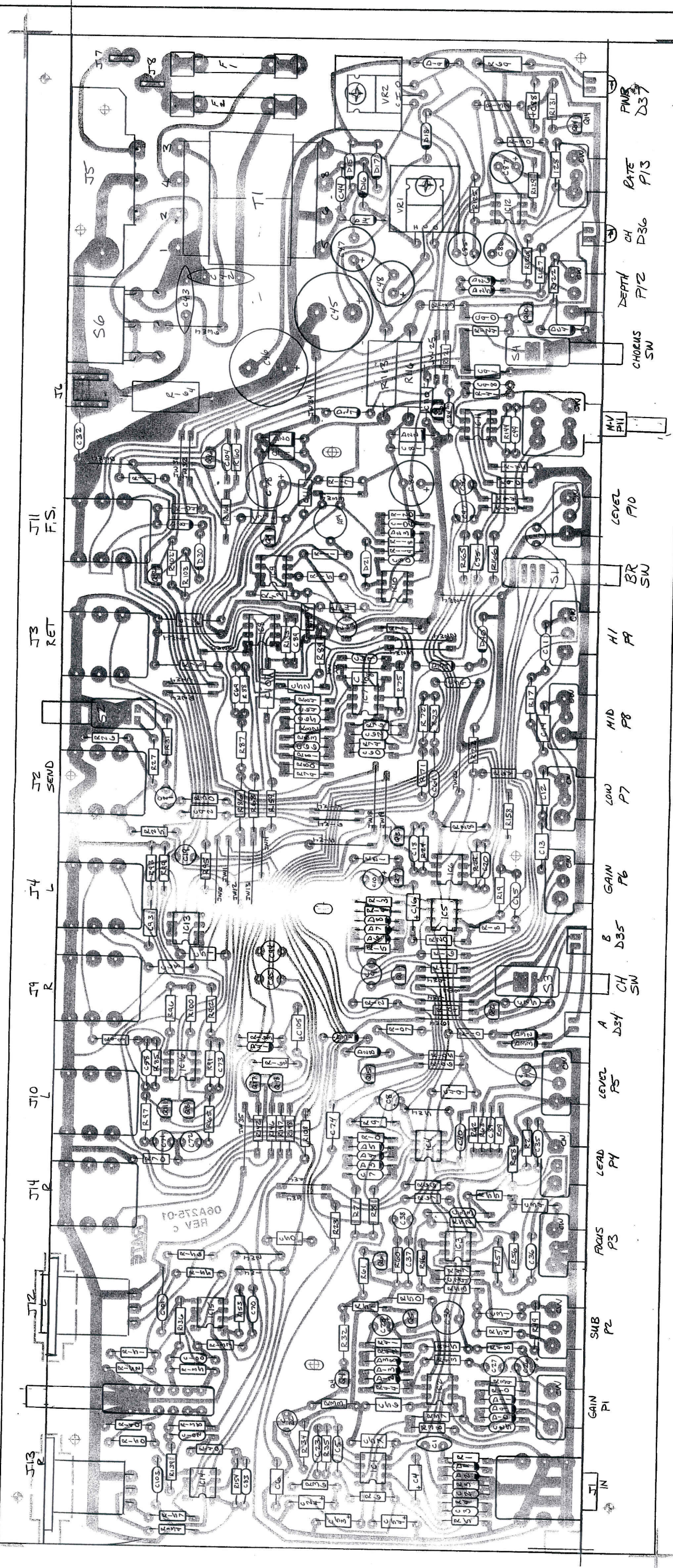


J1 IN
 J2 SEND
 J3 RET
 J4 L
 J5 R
 J6 L
 J7 R
 J8 F.S.
 J9 L
 J10 L
 J11 F.S.
 J12 L

GAIN P1
 SUB P2
 FOCUS P3
 LEAD P4
 LEVEL P5
 A D34
 B D35
 GAIN P6
 LOW P7
 MID P8
 HI P9
 BK SW P10
 LEVEL P10
 M.V. P11
 DEPTH P12
 CH P13
 RATE P13
 PWR P13
 D37

TOLERANCES		REVISIONS	
RESISTOR	CAPACITOR	NO.	DATE
±1%	±1%	1	
±5%	±5%	2	
±10%	±10%	3	
±20%	±20%	4	
±50%	±50%	5	
±100%	±100%	6	

DRAWN BY: XLP
 CHECKED BY: PWB
 DATE: 9-9-50
 MATERIAL: PRE-AMP
 APPROVED BY: D37
 DRAWING NO: 07A95-01



Assy # 07-275-01

SMT 1 of 2

TOLERANCES UNLESS OTHERWISE SPECIFIED		REVISIONS	
NO.	DATE	BY	DATE
1	02-14-71		
2			
3			
4			
5			

SLM ELECTRONICS
 11880 BORMAN DR. ST. LOUIS, MO 63146
 XLP PRE-AMP
 DRAWN BY: [Signature]
 CHECKED BY: [Signature]
 DATE: 9-9-90
 SHEET NO. 07-275-01

J1 IN
 GAIN P1
 SUB P2
 FOCUS P3
 LEAD P4
 LEVEL P5
 A D34 CH SW
 B D35
 GAIN P6
 LOW P7
 MID P8
 HI P9
 BR SW
 LEVEL P10
 MV P11
 CHOICUS SW
 DEPTH P12
 CH D36
 RATE P13
 PWR D37