

The schematic diagram illustrates a 12-lead ECG system. It features two input channels, IN1 and IN2, each equipped with a 50K log potentiometer (CV1, CV2) and a 50K log potentiometer (R1, R2). The signals are processed by two OPA2604 op-amp stages (IC1A, IC1B) and two LM358 op-amp stages (IC2A, IC2B). The circuit includes various resistors (R1-R21), capacitors (C1-C14), and a 12-lead ECG signal source (S1). The output is connected to a 12-lead ECG signal source (S1).