Б



 $\parallel$ 

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applied to any existing bias network. Now we need to work back towards the rectifier and transformer.

Before solid-state rectifiers were available, valve rectifiers were the order of the day. Most power rectifiers contained two diodes with separate anodes and a shared cathode/heater, so the familiar two-phase [rectifier](http://www.valvewizard.co.uk/bridge.html) arrangement was the most convenient. The bias supply demands hardly any current so it was sufficient to use a half-wave supply which required only one extra (relatively small) diode, rather than going to the expense of adding a fourth diode for a full-wave supply. When silicon rectifiers finally appeared they were simply substituted for the old valve rectifiers in many circuits, which is why the old fashioned half-wave bias supply is commonly used even in new amps.



It's important to realise that there is nothing special about the bias rectifier; it works just like the positive part of the power supply, but it is often drawn differently which makes it appear to have special status. Let's break it down. The circuit labelled **a.** is a conventional bipolar power supply. It looks like a bridge rectifier but really it is a pair of two-phase rectifiers –the pairs of diodes face opposite directions so one pair produces the positive rail while the other produces an equal-but-opposite negative rail. However, we don't need as much negative voltage for the bias supply, so let's put in a series resistor R1, to drop some voltage (this also means C1 can have a smaller voltage rating). This takes us to the image in **b.** Historically it was expensive to use two diodes, so let's throw one of them away, leaving us with the half-wave negative supply in **c.** Finally, the circuit in **d.** is identical to the one in **c.**, it's just been drawn the way you usually see it on amp schematics.

The series dropping resistor R1 forms a potential divider with the bias adjustment circuit and so reduces the average rectified voltage. Assuming we are using the half-wave rectifier and we need to drop the voltage quite a lot (which is usually true) we can use the following formula:

 $Vdc = Vrms \times 0.45 \times Rshunt / (Rshunt + Reseries)$ 

Putting this in terms of the triode mu we get to the unusual formula:

Rseries = Rshunt( $0.32 \times \mu - 1$ )

For example, if the bias adjustment circuit amounts to 20k to ground, and the mu of the power valves is 10, then we need a series dropping resistor of:

Rseries =  $20000 \times (0.32 \times 10 - 1) = 20000 \times (2.2) = 44000$  ohms

(the close standard of 47k would probably do since this equation is actually a slight underestimate). If you decide to use the full-wave version of the circuit then you will need twice the series resistance. This will give you exactly the same voltage and charge time, but less ripple and less stress on the power transformer. The completed circuit is shown below.



In some amplifiers the bias is taken from a low-voltage tapping point on the transformer winding. In such cases no series resistor (or a very small resistor) is required. In fact, it's more likely the voltage won't be great enough. If that is the case then either use one of the circuits above, or convert to a voltage doubler as shown below (you may prefer to use larger values for C1 and C3, otherwise the ripple doubles too).

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## **Capacitor Cupled Bias Supply**

When you need a bias supply but your transformer has no centre tap (i.e. bridge rectifier) then a cheap dodge is to use a capacitorcoupled bias supply. The image below shows the basic arrangement with current paths illustrating how it works (R2 represents the bias adjust circuit).



The coupling capacitor C1 initially charges up through R1. Beginners often forget this resistor; it is essential! The bigger you make C1, the greater the bias voltage achieved; a good compromise is 47n to 220n. It needs to be rated for more than the peak AC transformer voltage. Use a good quality plastic/poly capacitor. The optimum condition is then to make R1 equal to about 33k to 47k as this allows the largest negative voltage to be achieved.

On the next half cycle C1 it discharges through D1, into C2. This allows C2 to charge negatively. The size of C2 does not affect the final bias voltage, only the ripple and charging time –anything from 10u to 47u is typical.

Notice that this charging path includes the HT load represented by R3. This means that before the valves have warmed up, there will be no bias voltage present unless you have a bleeder resistor across the HT. It is therefore a good idea to include a bleeder, and 100k to 220k (2W) is ideal. (Also, the HT reservoir capacitor must be larger than C1, but it always is anyway, so don't worry about it).

The bigger you make R2 (the bias adjust circuit), the greater the available negative voltage. It is a good idea to make it larger than R1 otherwise loading becomes excessive. However, it can't be *too* large since it forms part of the output valves' grid leak path. This also makes this sort of bias supply unsuitable for powerful amplifiers with big/multiple output valves that need a low-impedance grid leak path.

A good compromise is to use a 47k bias pot; an end-stop resistor will then raise the total above R1. This should be fine for anything up to 50W amp. The completed circuit is shown on the right (drawn in the more familiar arrangement) with a typical bias-adjust network. This circuit is fairly



universal and will generate a negative voltage a little greater than one tenth of the HT, making it suitable for use with most power valves. C1 can be increased if more negative voltage is needed.