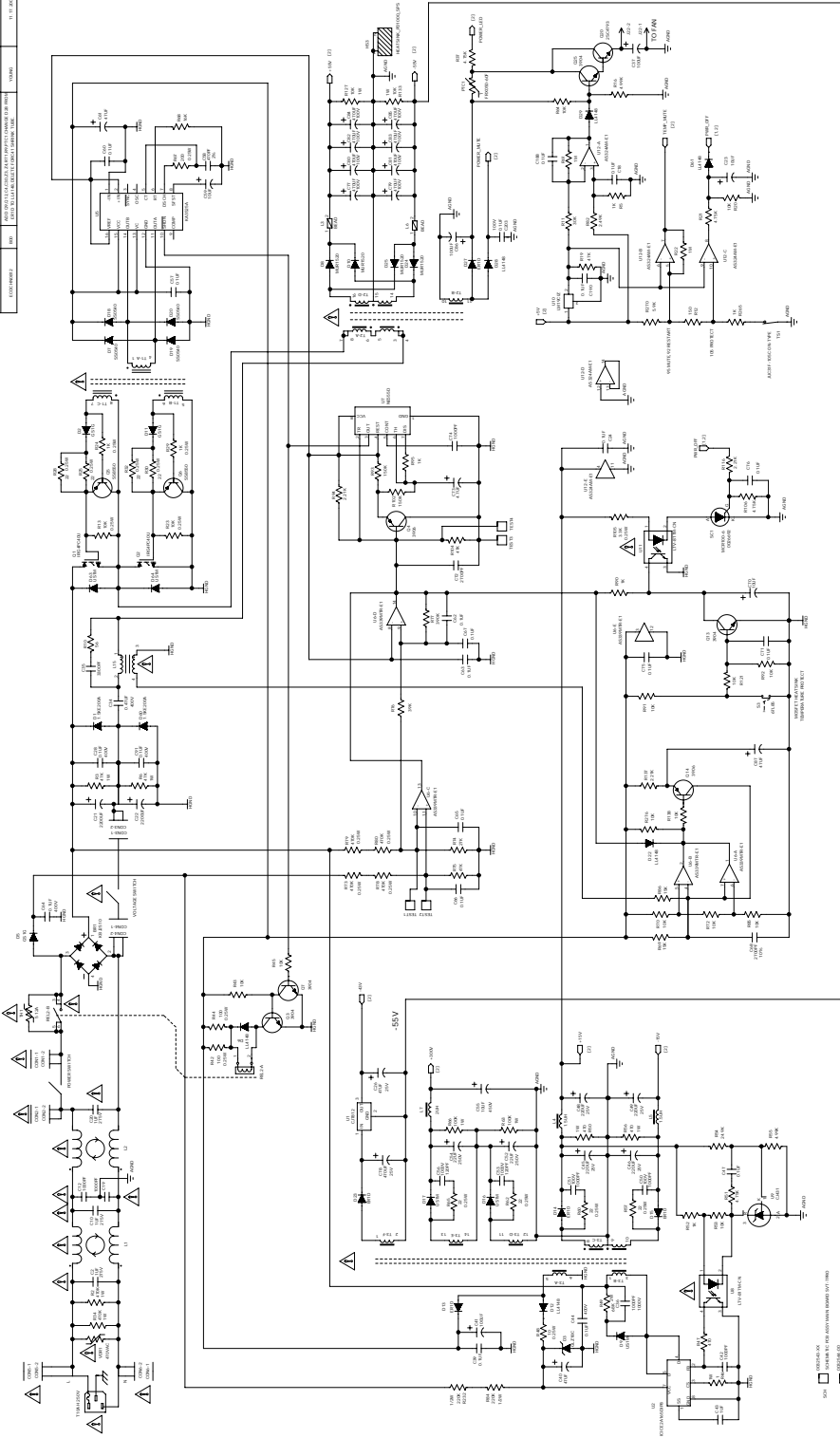
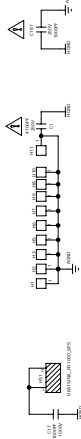



SYMBOL	DESCRIPTION	QTY	U.S.A.
RESISTOR	RESISTOR	VARIOUS	VARIOUS
CAPACITOR	CAPACITOR	VARIOUS	VARIOUS
DIODE	DIODE	VARIOUS	VARIOUS
TRANSISTOR	TRANSISTOR	VARIOUS	VARIOUS
INTEGRATED CIRCUIT	INTEGRATED CIRCUIT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS
WELDED CONTACT	WELDED CONTACT	VARIOUS	VARIOUS



UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN MILLIMETERS (DIMENSIONS IN PARENTHESES ARE IN INCHES).
 ALL DIMENSIONS ARE TO CENTER UNLESS OTHERWISE SPECIFIED.
 DIMENSIONS ARE TO BE TAKEN FROM THE CENTER OF THE COMPONENT UNLESS OTHERWISE SPECIFIED.
 DIMENSIONS ARE TO BE TAKEN FROM THE CENTER OF THE COMPONENT UNLESS OTHERWISE SPECIFIED.
 DIMENSIONS ARE TO BE TAKEN FROM THE CENTER OF THE COMPONENT UNLESS OTHERWISE SPECIFIED.



REV.	DATE	BY
1.0	03/24/83	JK



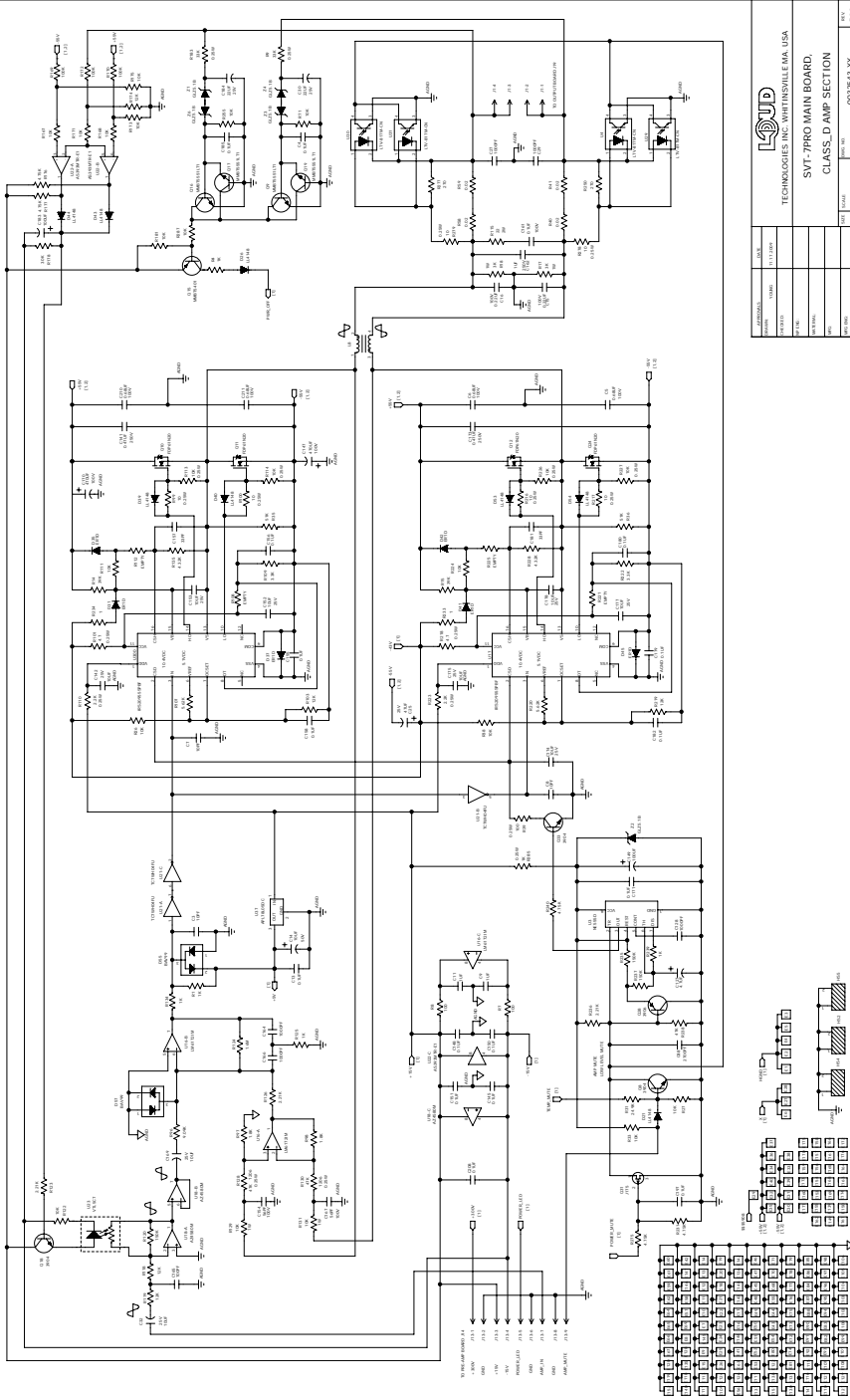
LOGIC TECHNOLOGIES, INC.
 WOODBRIDGE, MA, USA

SVT-7 PRO MAIN BOARD

POWER SUPPLY SECTION

PROJECT NO.	03-24-83-XX
DESIGNED BY	JK
CHECKED BY	JK
DATE	03/24/83
REV.	1.0

REV.	DESCRIPTION	DATE
1.0	INITIAL DESIGN	11.13.00
1.1	REVISIONS TO BOARD	11.13.00
1.2	REVISIONS TO BOARD	11.13.00
1.3	REVISIONS TO BOARD	11.13.00
1.4	REVISIONS TO BOARD	11.13.00
1.5	REVISIONS TO BOARD	11.13.00
1.6	REVISIONS TO BOARD	11.13.00
1.7	REVISIONS TO BOARD	11.13.00
1.8	REVISIONS TO BOARD	11.13.00
1.9	REVISIONS TO BOARD	11.13.00
1.10	REVISIONS TO BOARD	11.13.00



LOUD
TECHNOLOGIES INC. QUINCYVILLE MA USA

**SVT-7PRO MAIN BOARD,
CLASS. D AMP SECTION**

REV: 1.10
DATE: 11/13/00
AUTHOR: JAC

REV: 1.10
DATE: 11/13/00
AUTHOR: JAC

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DATE: 11/13/00
AUTHOR: JAC

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